

**ABSTRACT OF THE DISCLOSURE**

The present technique relates to a method and apparatus for detecting a change in a data signal at a buffer device. In the buffer device, first stage comparators may be adapted to receive a data signal and either a first voltage timing reference (VTR) signal or a complimentary VTR signal. The first stage comparators may each deliver an output signal to second stage comparators. Each of the second stage comparators receives the output signal from each of the first stage comparators. From the first stage comparator signals, the second stage comparators produce an output signal, such as a first output signal and a second output signal. These output signals from the second stage comparators are differential signals.